

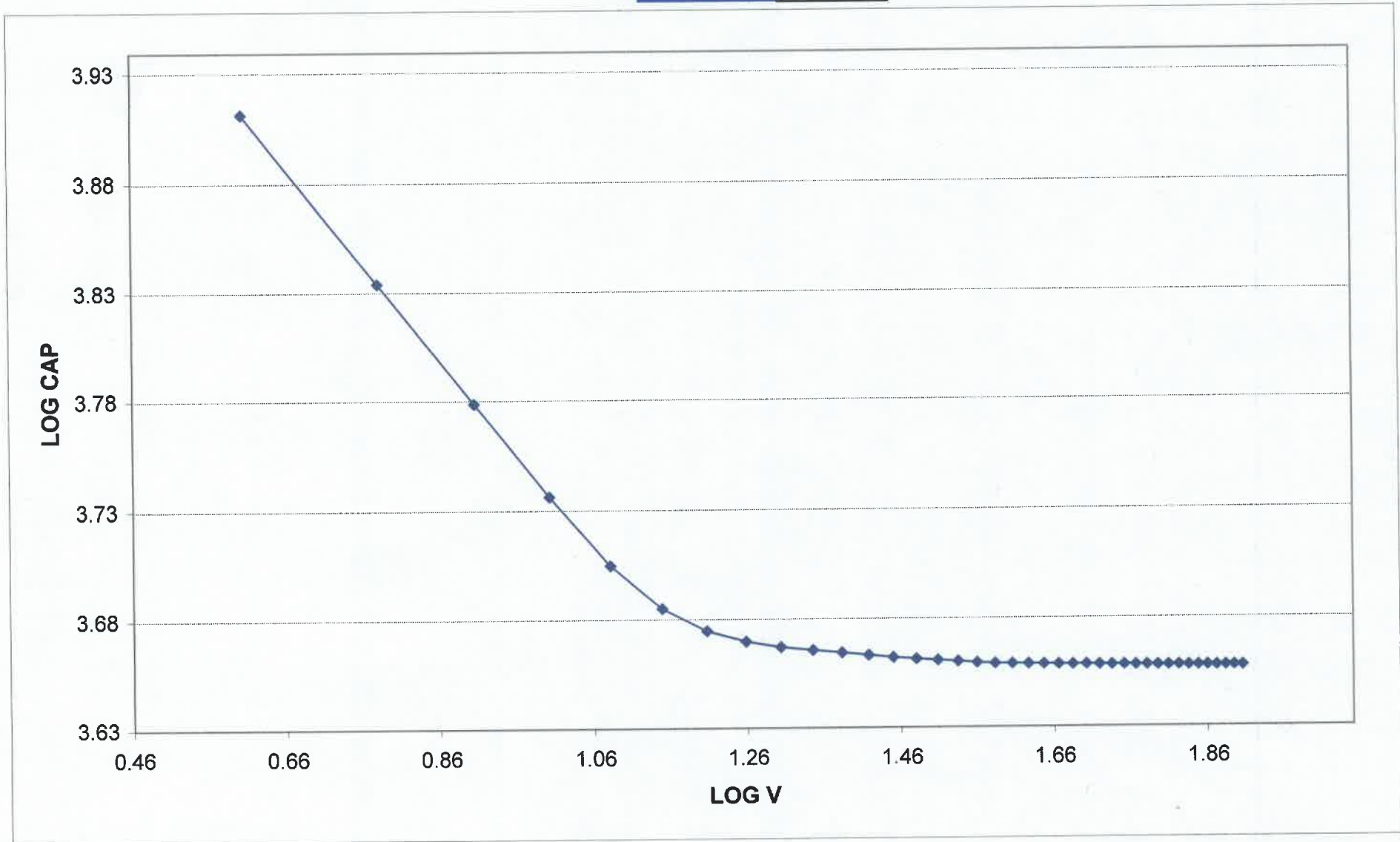
Depletion Plot

Design-W_60

Wafer No.: **3187-5**

Thickness: **60** um

Depletion: **25** Volts

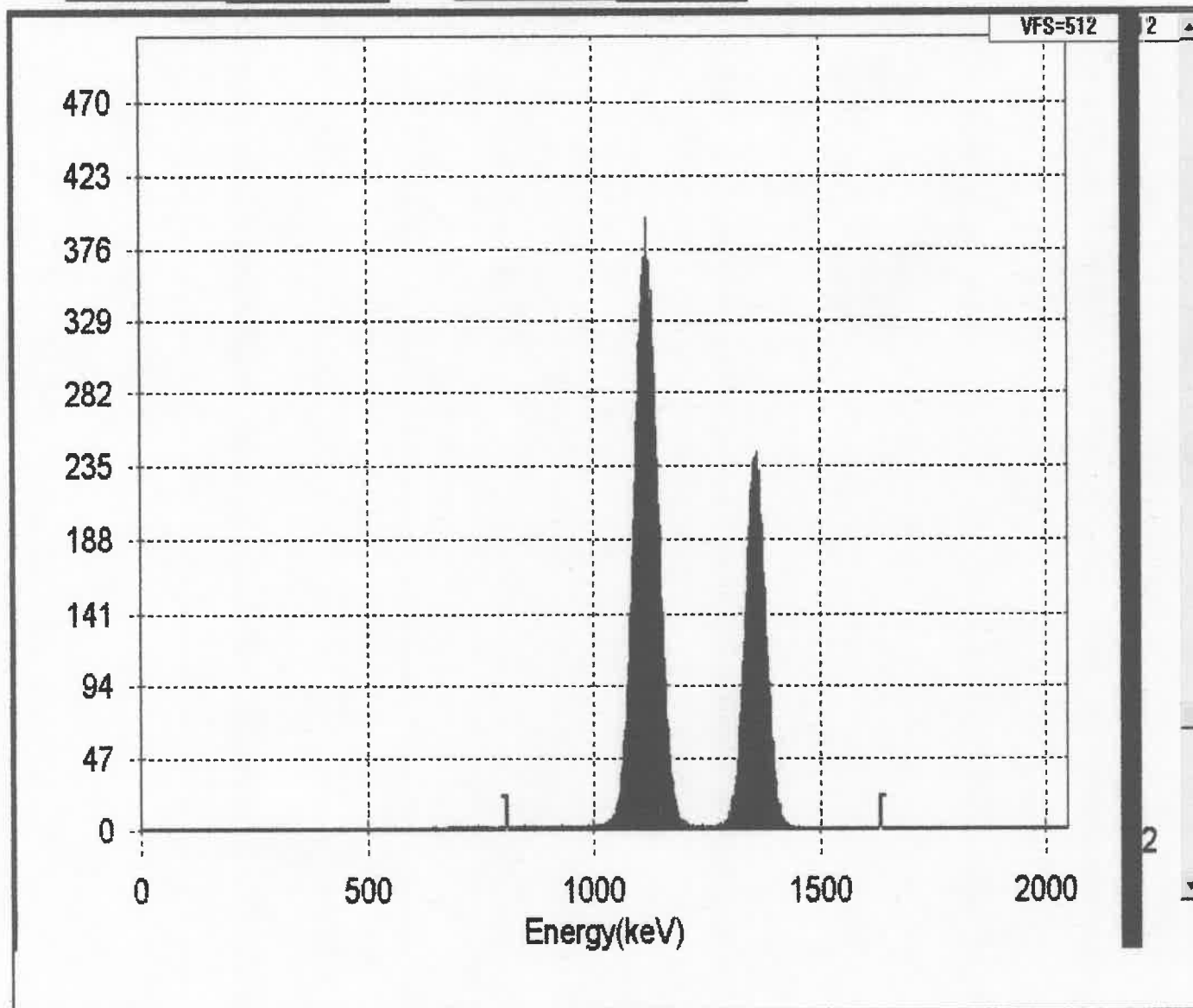


Resolution Plot

DESIGN W1-60 TYPE 9G

Wafer No.: **3187-5**

Thickness: **60** um



Source
Am 241

Rise Time
1

Flat Top
0

T=22.1DC

BIAS VOLTS= **30** V

Leakage **50** nA

Micron Semiconductor
07/09/2015

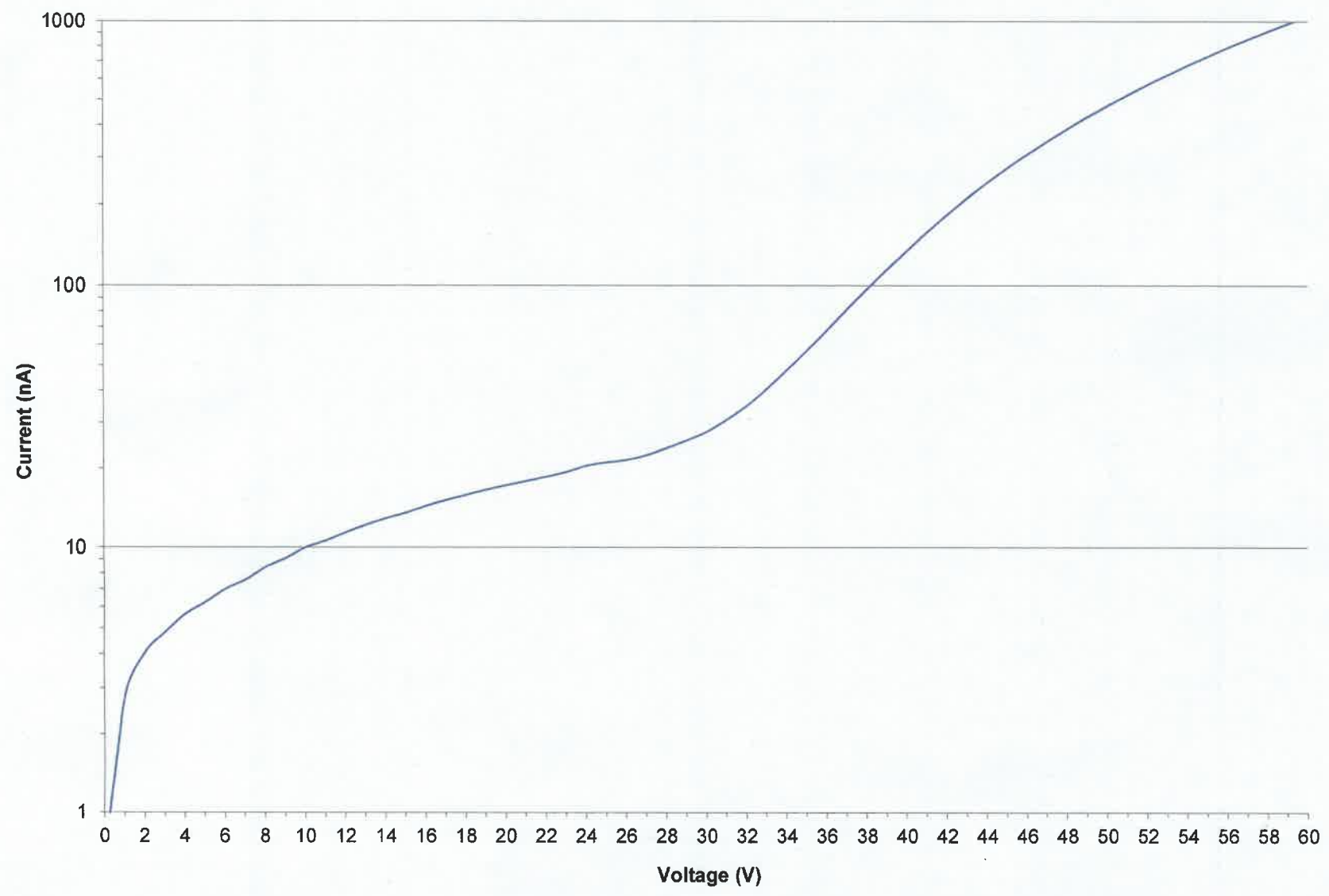
JUNCTION

DET LINE: **209** KeV
SYSTEM: **173** KeV
CAL: **116** KeV

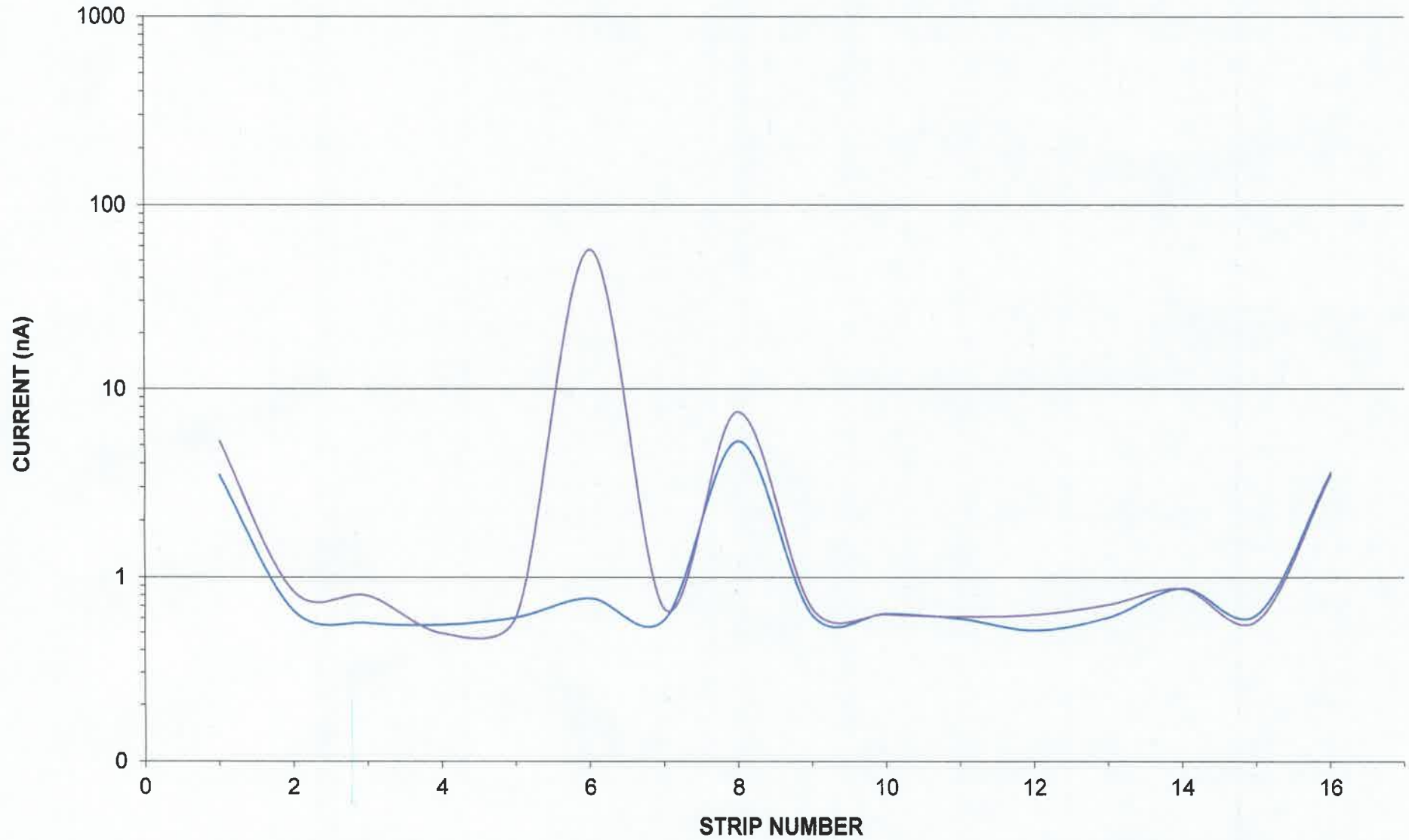
OHMIC

DET LINE: **210** KeV
SYSTEM: **172** KeV
CALC: **120** KeV

Design-W D/S
3187-5 TOTAL DETECTOR IV
Dep=25V Thickness=60um

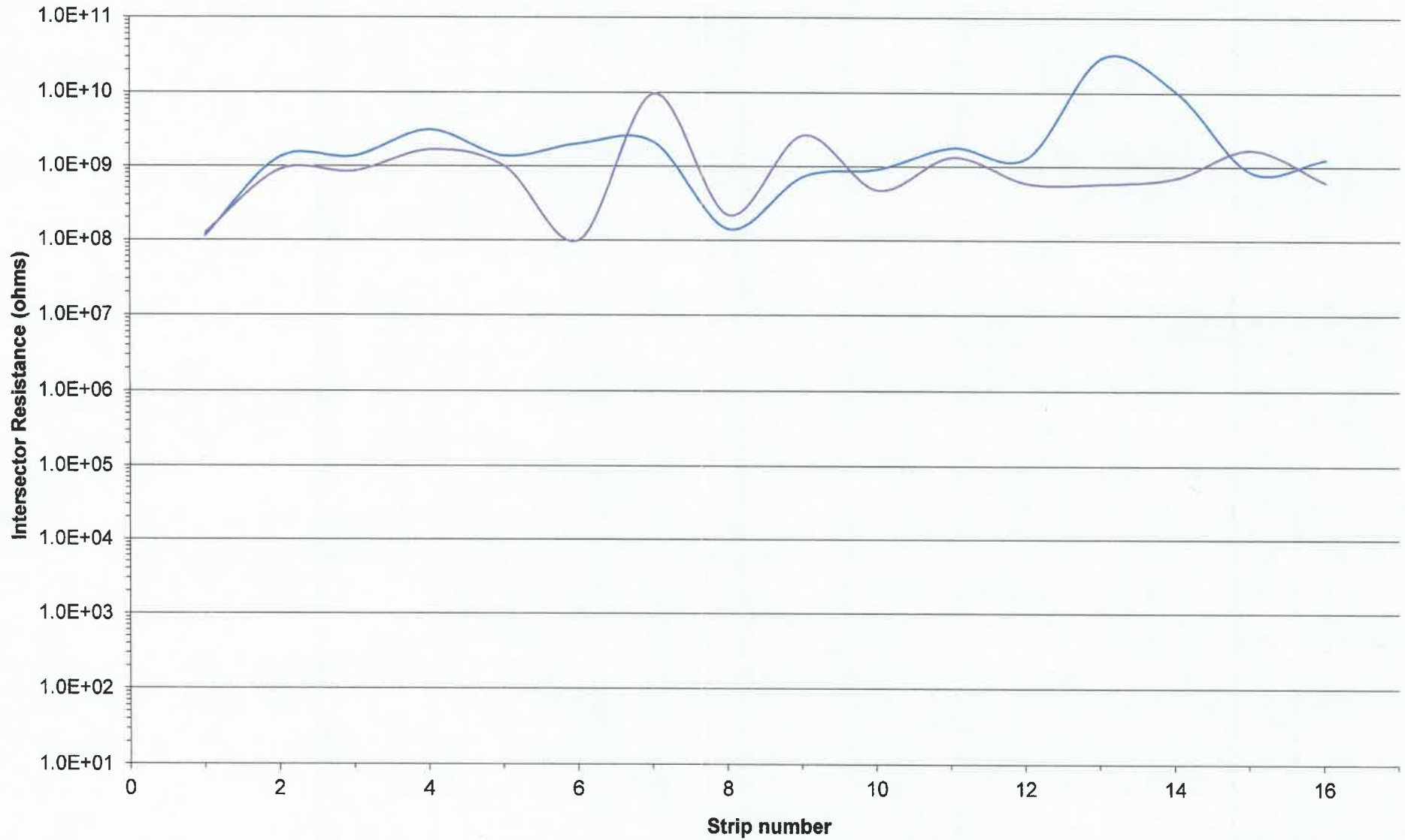


Design-W N-TYPE
3187-5 Junction side Dep=25V Thickness=60um



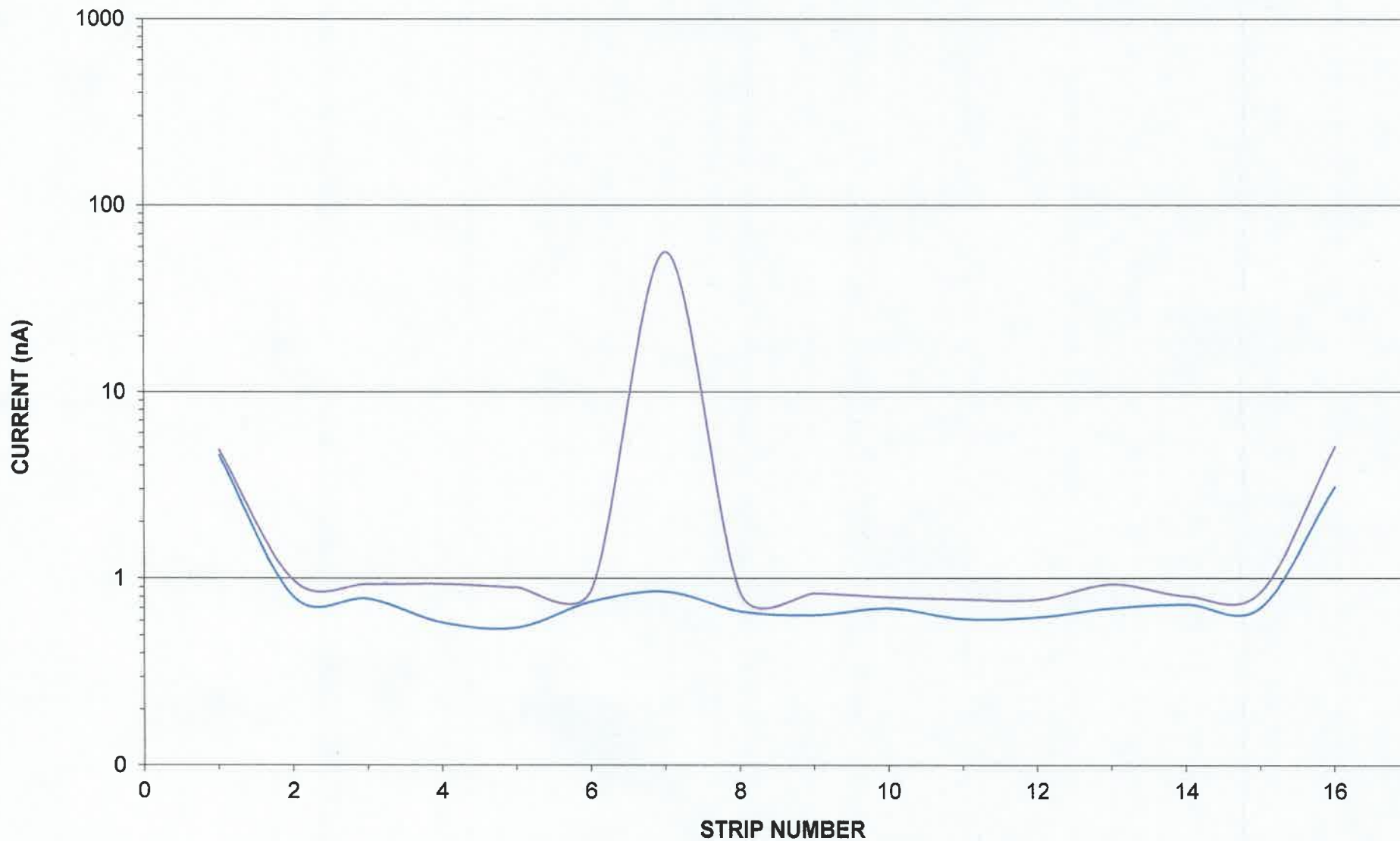
— current @ Dep — current @ Dep+50%

Design-W N-TYPE
3187-5 Junction side Dep=25V Thickness=60um



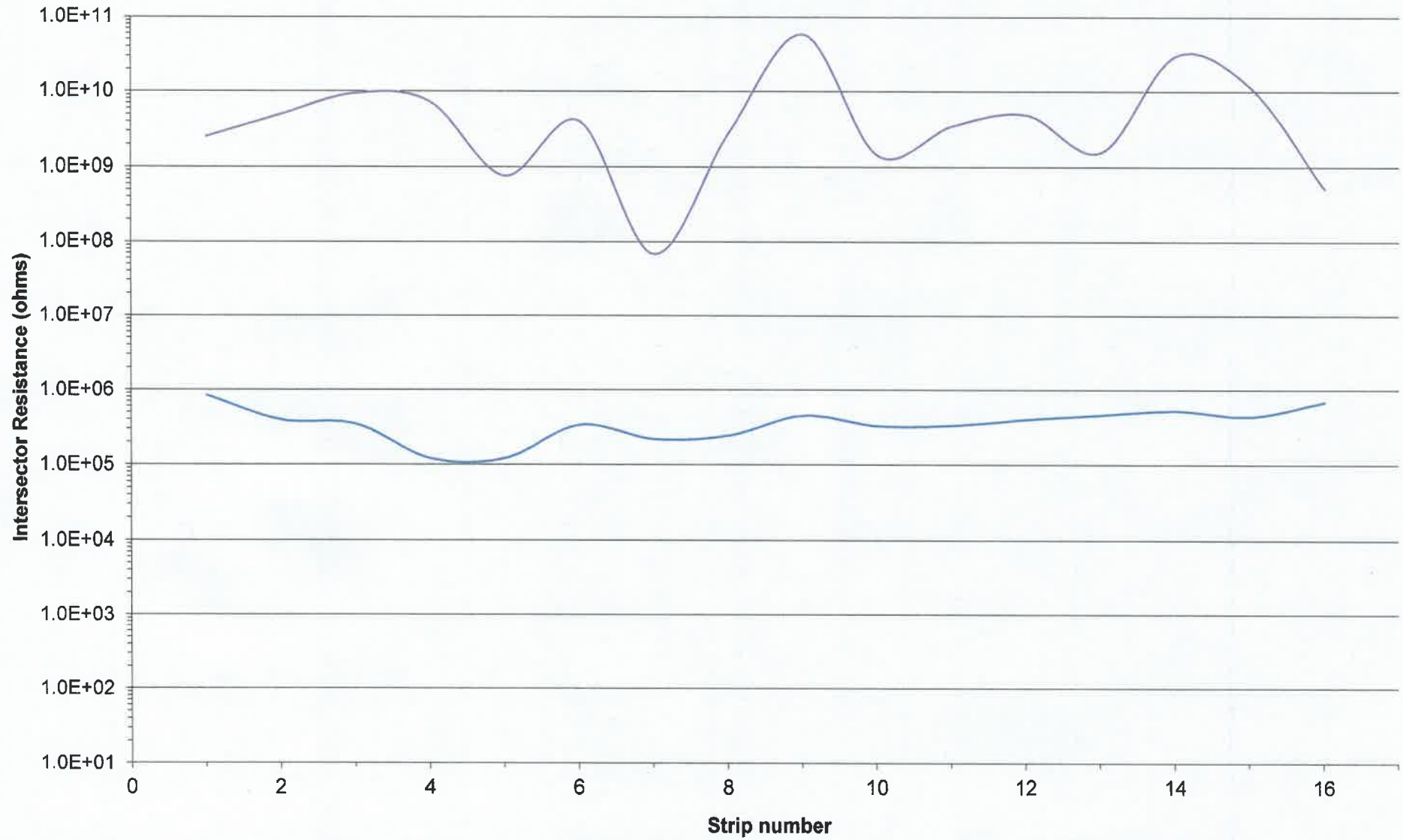
— Inter @ Dep — Inter @ Dep+50%

Design-W N-TYPE
3187-5 Ohmic side Dep=25V Thickness=60um



— current @ Dep — current @ Dep+50%

Design-W N-TYPE
3187-5 Ohmic side Dep=25V Thickness=60um



— Inter @ Dep — Inter @ Dep+50%