

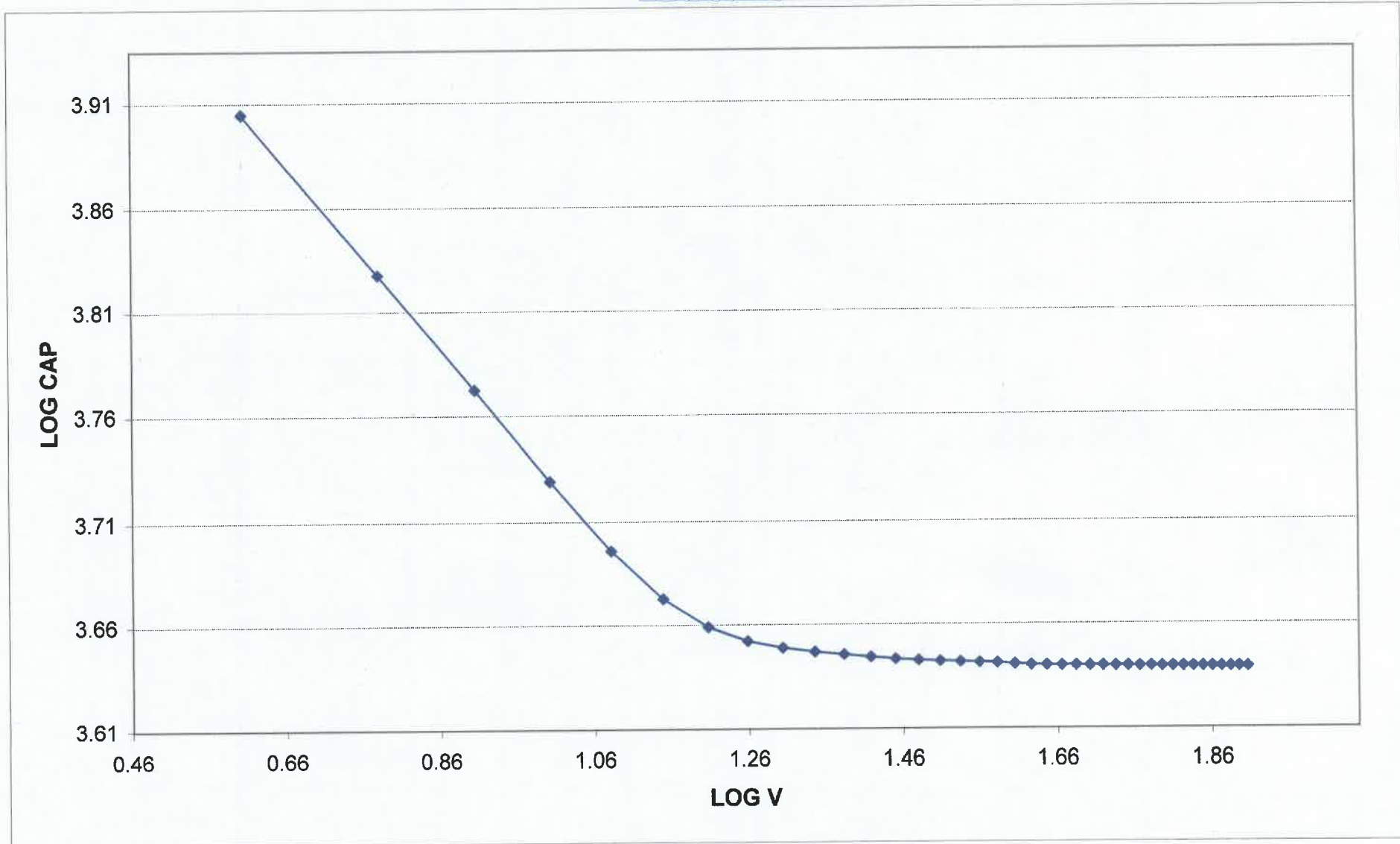
# Depletion Plot

Design-W\_60

Wafer No.: **3187-4**

Thickness: **60** um

Depletion: **20** Volts



# Resolution Plot

DESIGN W1-60 TYPE9G

Wafer No.: **3187-4**

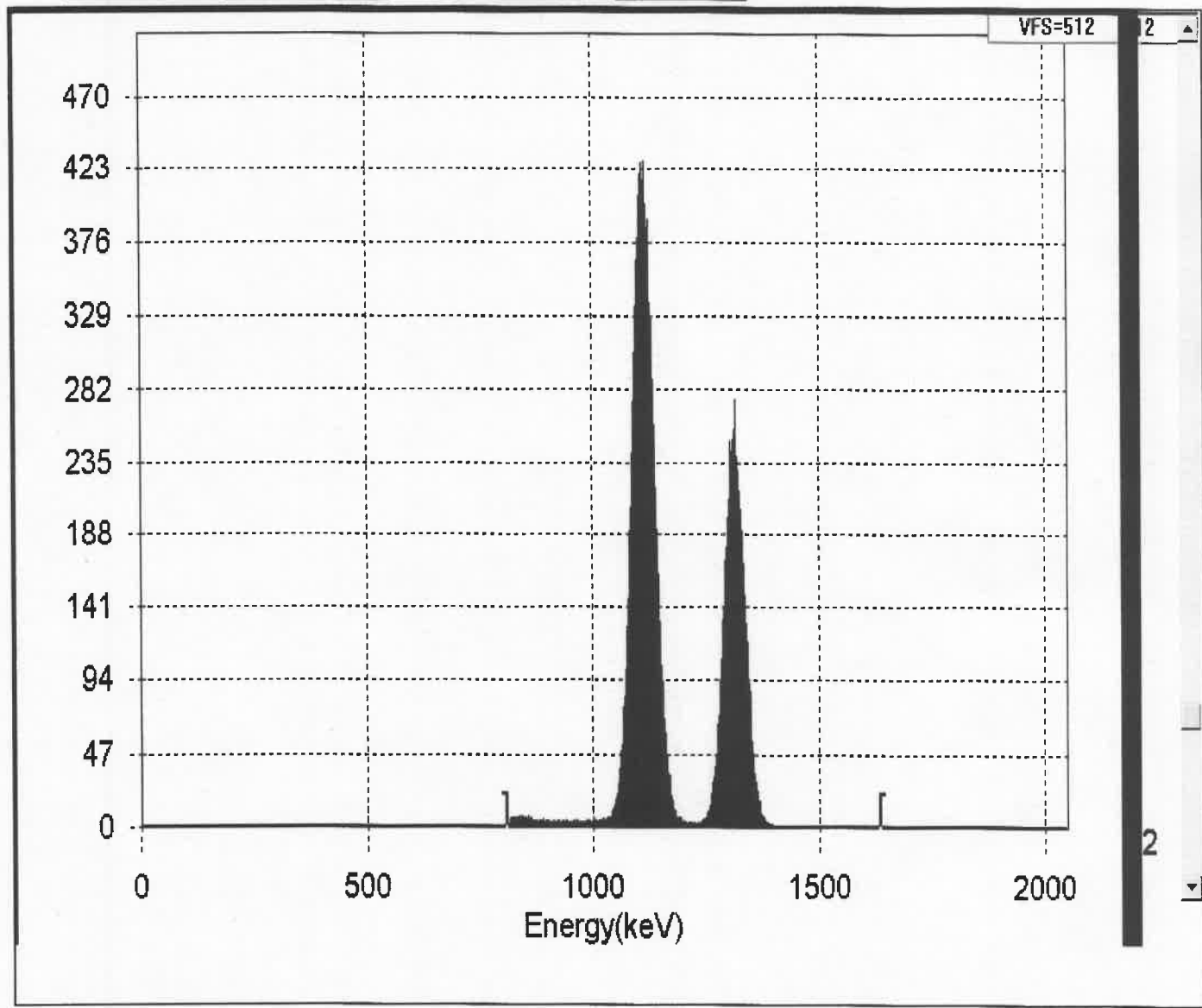
Thickness: **60** um

### JUNCTION

DET LINE: **201** KeV  
SYSTEM: **183** KeV  
CAL: **83.1** KeV

### OHMIC

DET LINE: **205** KeV  
SYSTEM: **183** KeV  
CALC: **92.3** KeV



Source  
**Am 241**

Rise Time  
**1**

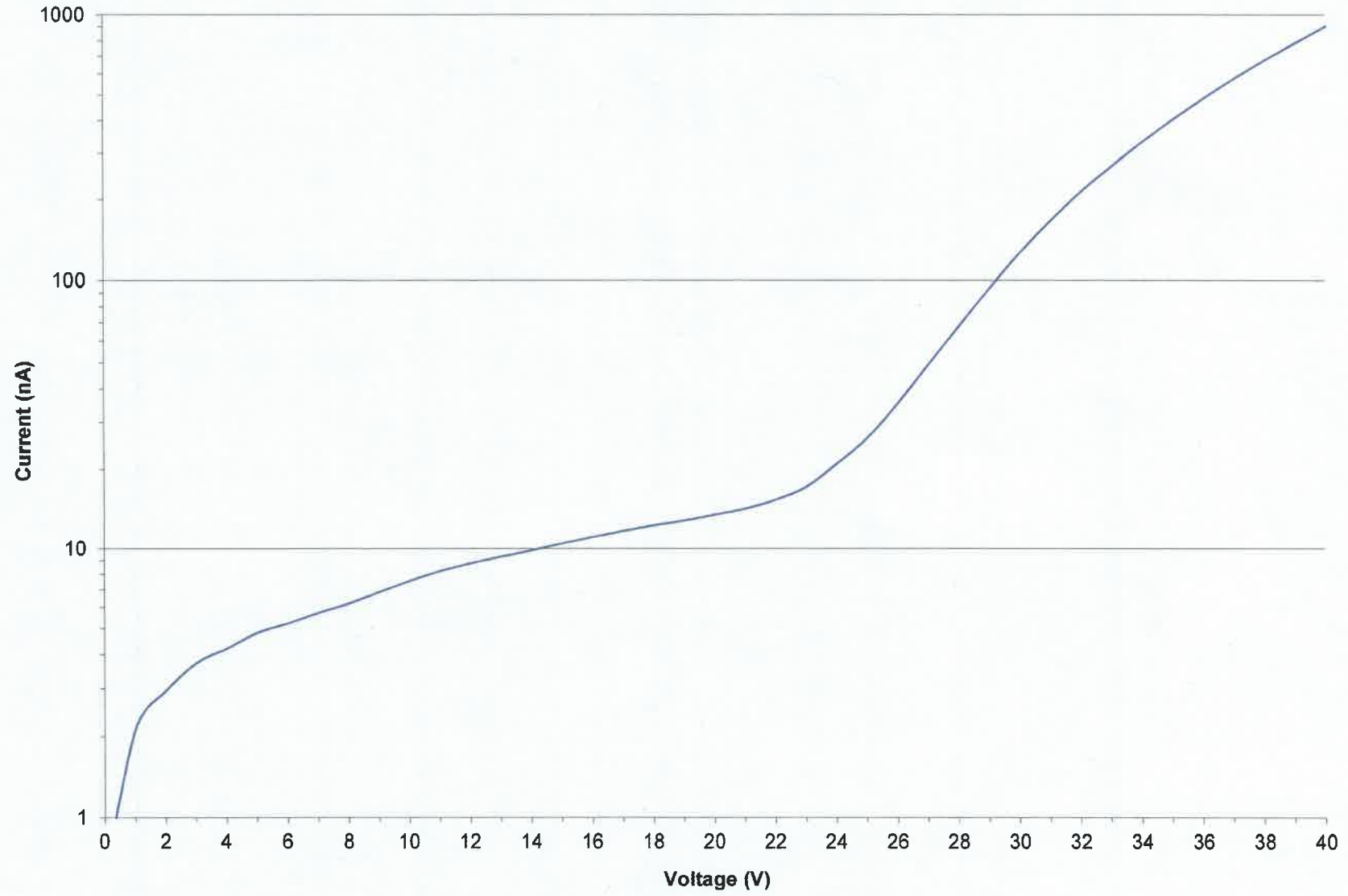
Flat Top  
**0**

T=22.1DC

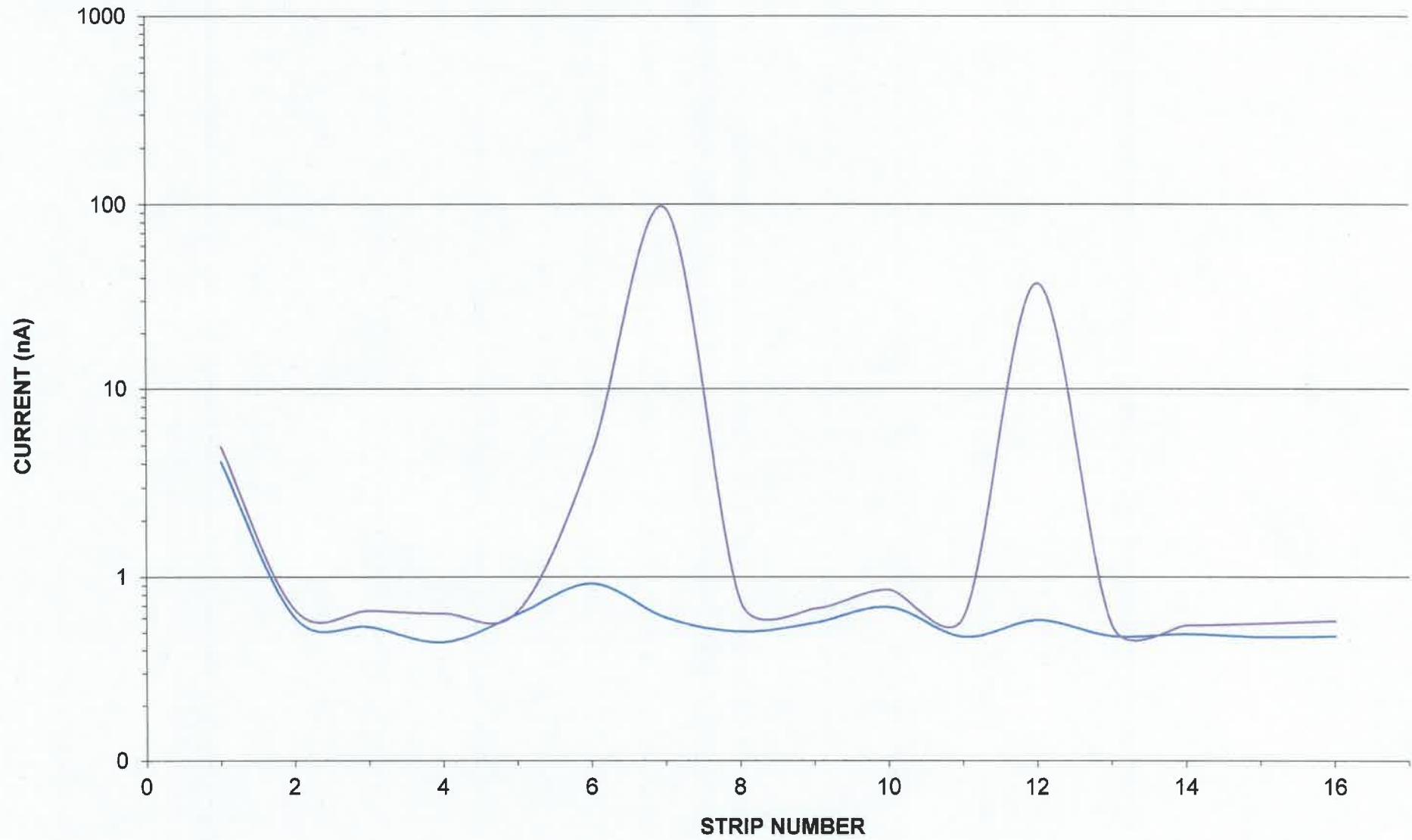
BIAS VOLTS= **30** V

Leakage **35** nA

Design-W D/S  
3187-4 TOTAL DETECTOR IV  
Dep=20V Thickness=60um

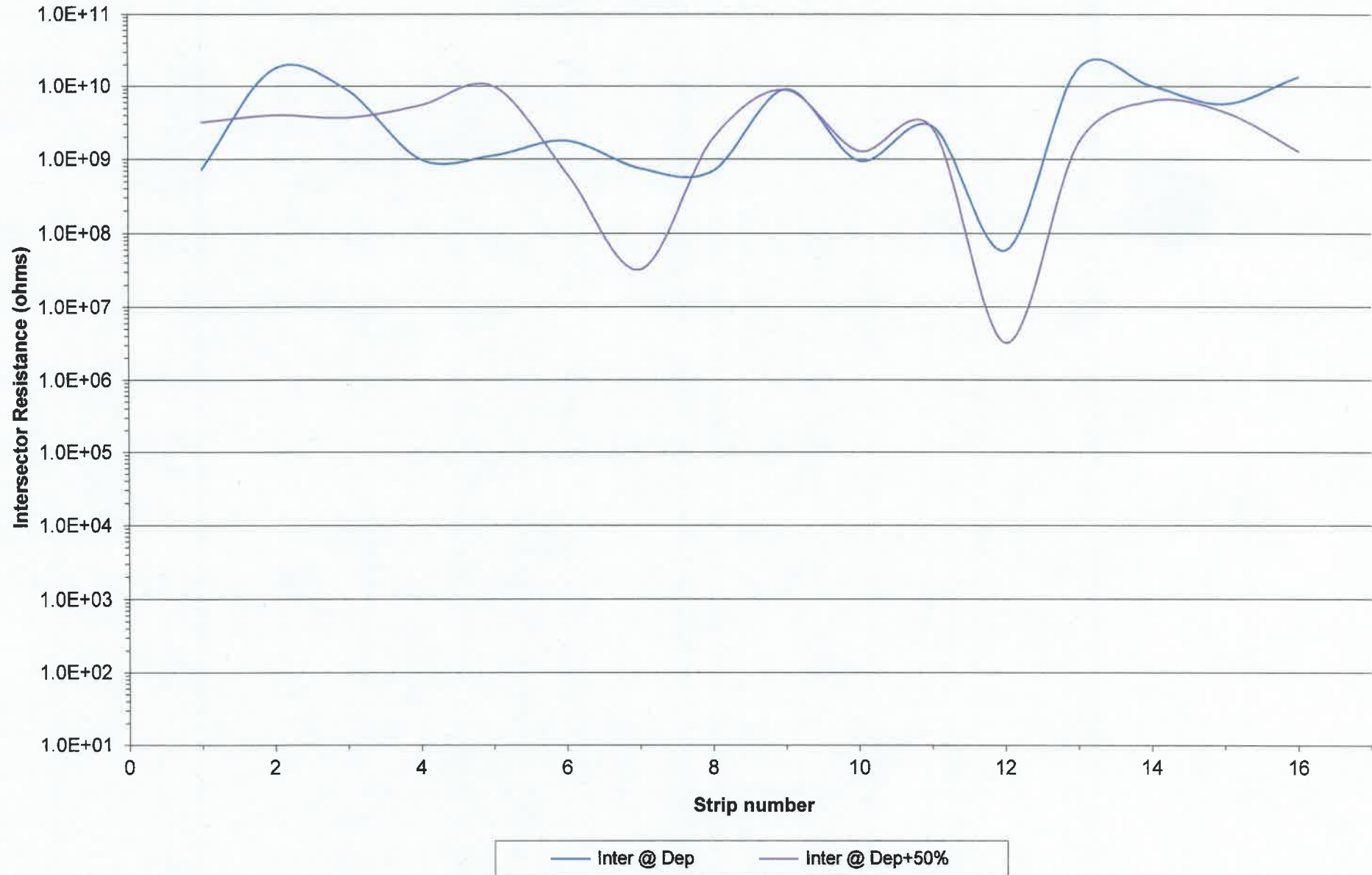


Design-W N-TYPE  
3187-4 Junction side Dep=20V Thickness=60um

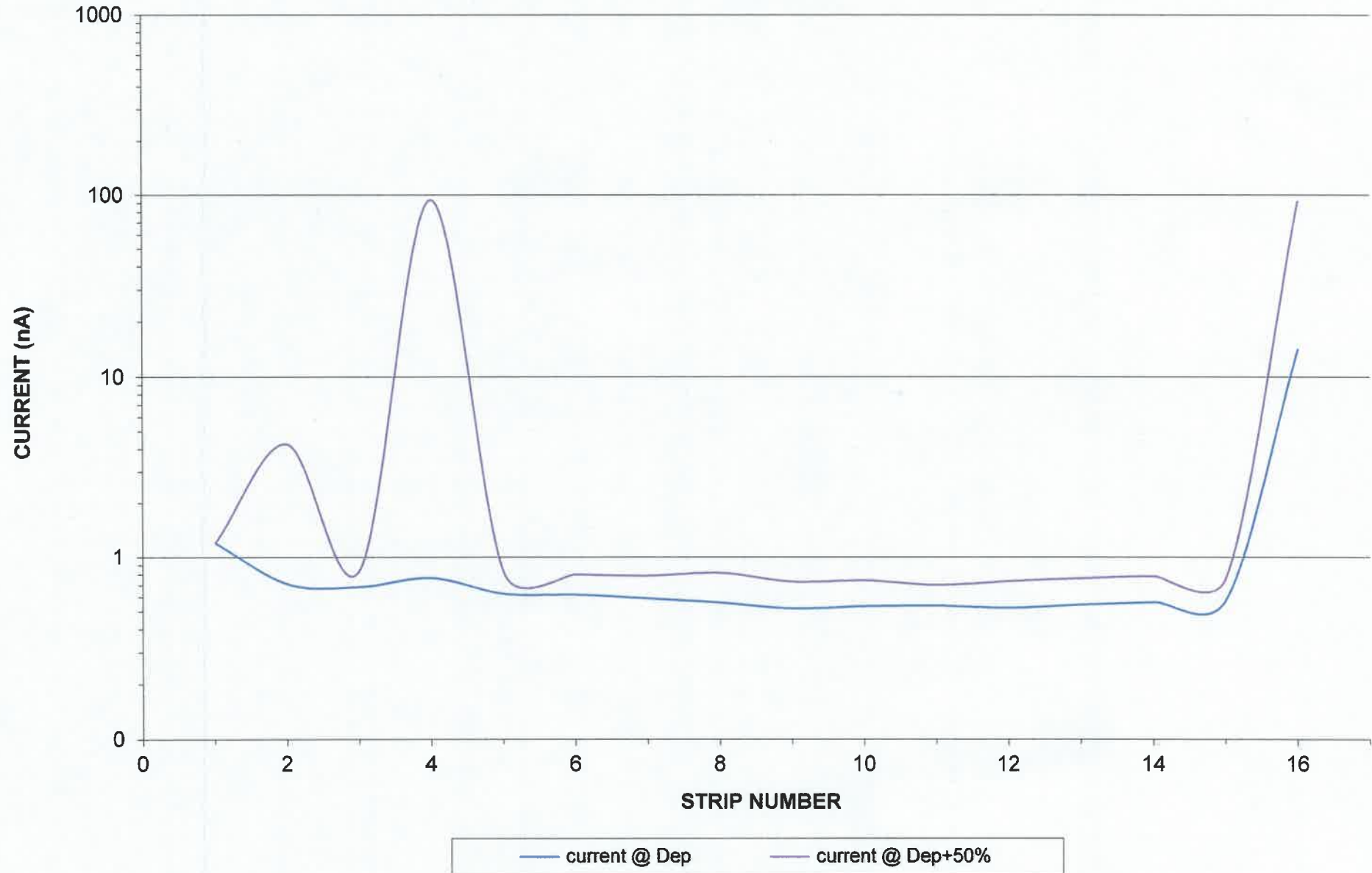


— current @ Dep      — current @ Dep+50%

Design-W N-TYPE  
3187-4 Junction side Dep=20V Thickness=60um



Design-W N-TYPE  
3187-4 Ohmic side Dep=20V Thickness=60um



**Design-W N-TYPE**  
**3187-4 Ohmic side Dep=20V Thickness=60um**

