

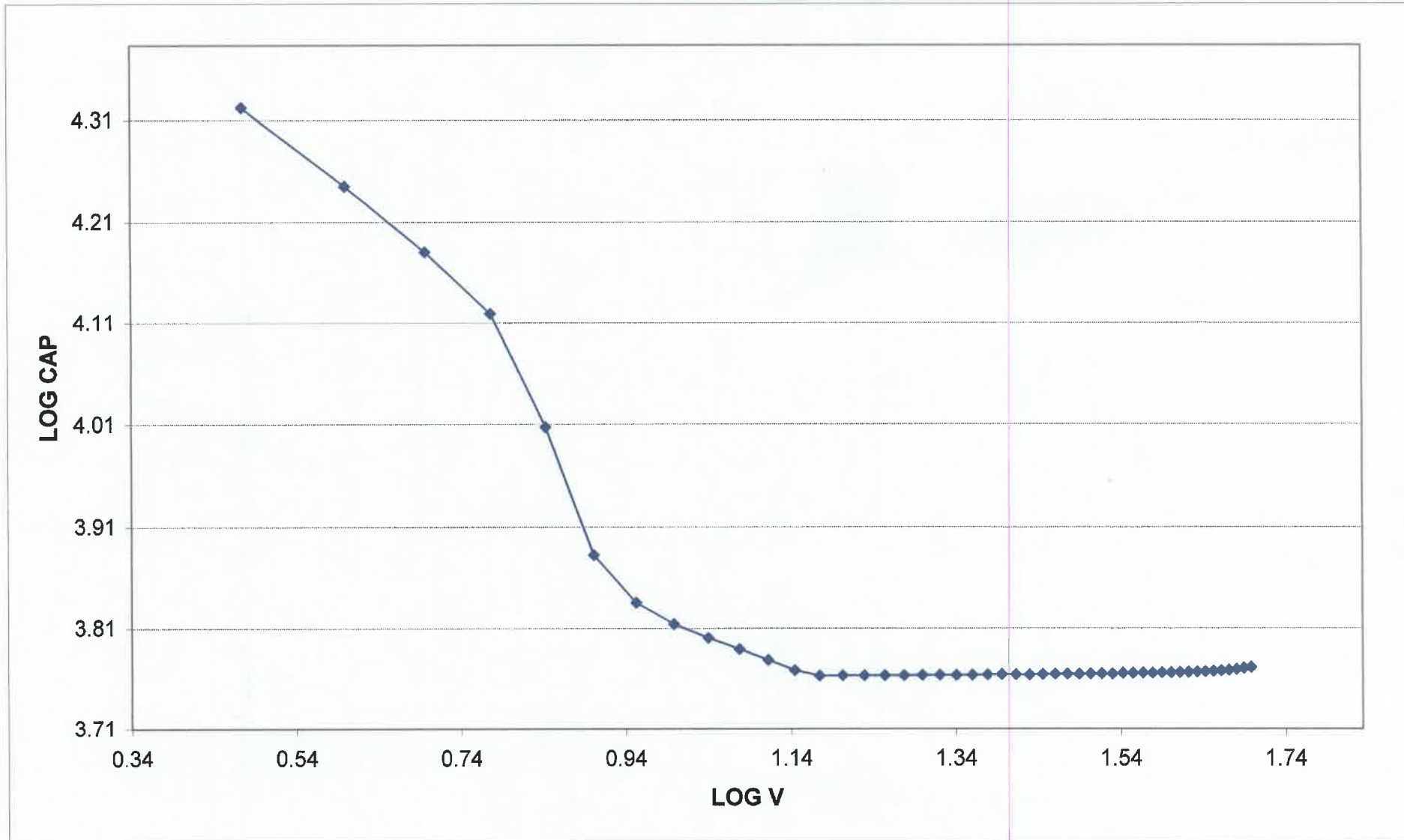
# Depletion Plot

## S3-60

Wafer No.: **2915-5**

Thickness: **64**  $\mu\text{m}$

Depletion: **14** Volts



Front Side Data

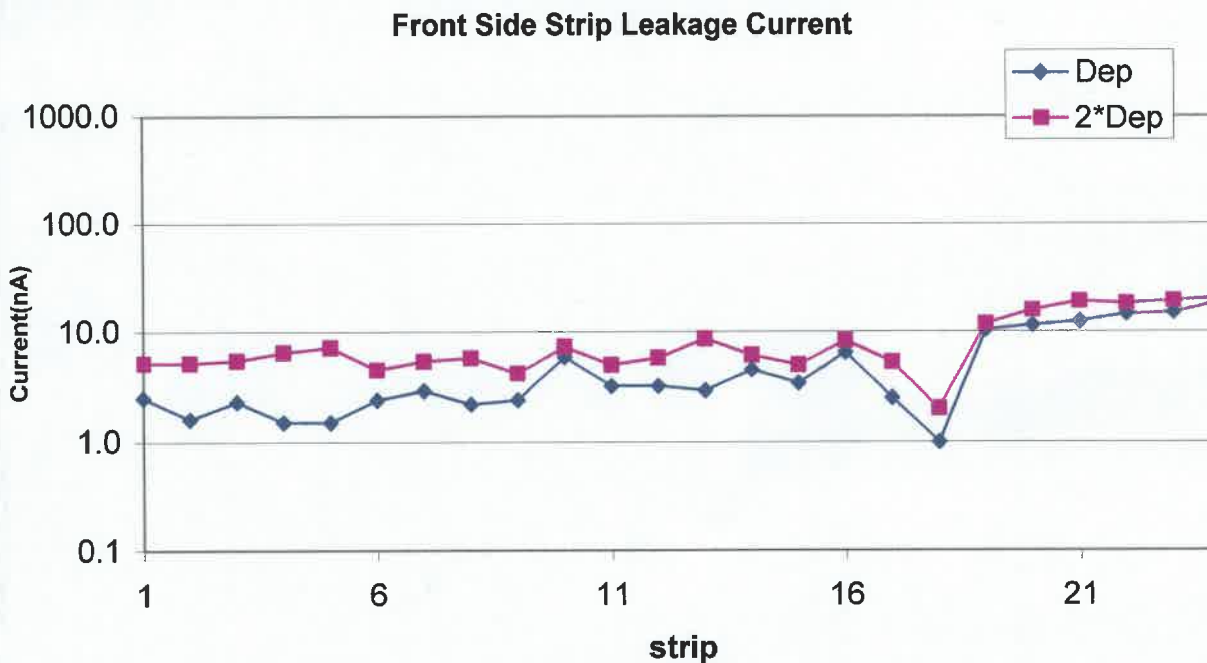
Wafer No.: **2915-5**

Thickness: **64** um

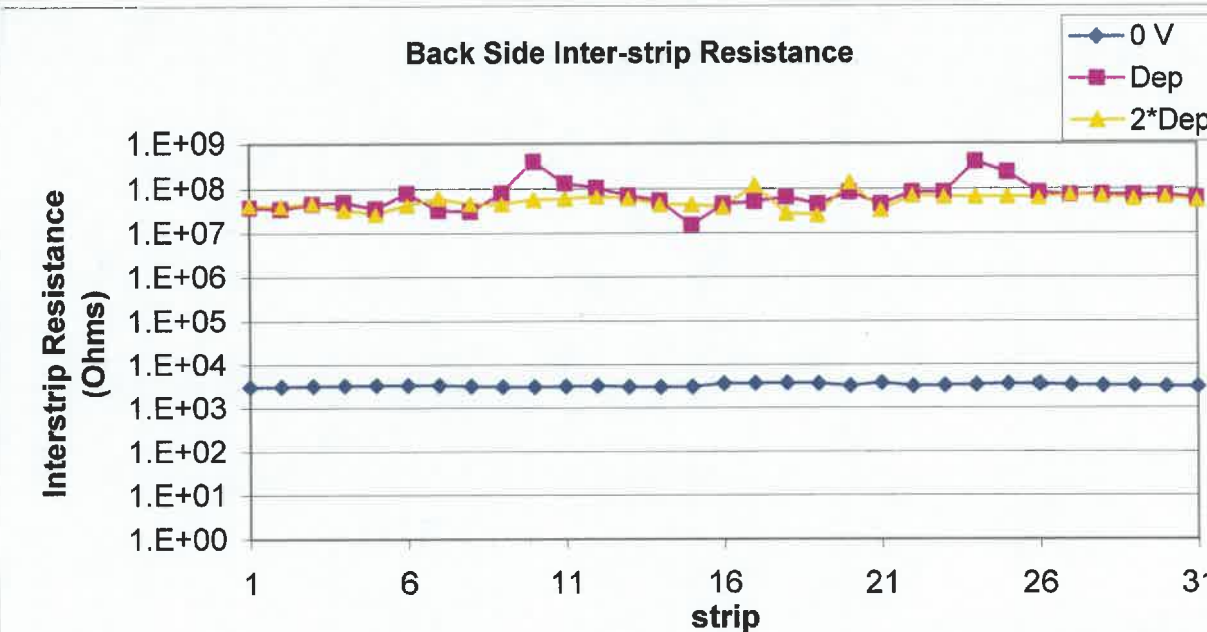
Depletion: **14** Volts

Back Side Data

Strip	Current (nA)	
	DEP	2*Dep
1	2.50	5.20
2	1.60	5.20
3	2.30	5.50
4	1.50	6.50
5	1.50	7.20
6	2.40	4.50
7	2.90	5.40
8	2.20	5.80
9	2.40	4.20
10	5.90	7.30
11	3.20	5.00
12	3.20	5.80
13	2.90	8.60
14	4.50	6.10
15	3.40	5.00
16	6.50	8.30
17	2.50	5.30
18	1.00	2.00
19	10.30	11.70
20	11.40	15.80
21	12.40	19.10
22	14.50	18.20
23	15.00	19.30
24	18.90	20.50
total	134.90	207.50



Strip	Back Resistance (Ω)		
	0V	DEP	2*Dep
1	3.1E+03	3.8E+07	4.2E+07
2	3.1E+03	3.5E+07	4.0E+07
3	3.3E+03	4.5E+07	4.7E+07
4	3.3E+03	4.9E+07	3.4E+07
5	3.3E+03	3.6E+07	2.7E+07
6	3.4E+03	7.9E+07	4.4E+07
7	3.4E+03	3.3E+07	6.1E+07
8	3.2E+03	3.1E+07	4.5E+07
9	3.1E+03	8.0E+07	4.6E+07
10	3.1E+03	4.0E+08	5.7E+07
11	3.2E+03	1.3E+08	5.9E+07
12	3.2E+03	1.1E+08	6.7E+07
13	3.1E+03	7.1E+07	6.2E+07
14	3.1E+03	5.4E+07	4.5E+07
15	3.1E+03	1.5E+07	4.4E+07
16	3.7E+03	4.5E+07	3.9E+07
17	3.7E+03	5.2E+07	1.2E+08
18	3.7E+03	6.5E+07	2.8E+07
19	3.6E+03	4.6E+07	2.6E+07
20	3.2E+03	8.4E+07	1.4E+08
21	3.8E+03	4.5E+07	3.4E+07
22	3.2E+03	8.4E+07	7.1E+07
23	3.3E+03	8.4E+07	6.7E+07
24	3.4E+03	4.0E+08	6.6E+07
25	3.6E+03	2.4E+08	6.7E+07
26	3.6E+03	8.4E+07	6.4E+07
27	3.4E+03	7.3E+07	7.5E+07
28	3.3E+03	7.5E+07	6.9E+07
29	3.2E+03	7.4E+07	6.1E+07
30	3.1E+03	7.3E+07	6.7E+07
31	3.1E+03	6.3E+07	5.5E+07



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# Resolution Plot

DESIGN PAVIA S3-60

Wafer No.: **2915-5**

Thickness: **64** um

VFS=1024 ▲12 ▲

## JUNCTION

DET LINE: **165** KeV  
SYSTEM: **130** KeV  
CAL: **100** KeV

## OHMIC

DET LINE: **167** KeV  
SYSTEM: **130** KeV  
CALC: **104** KeV

Source

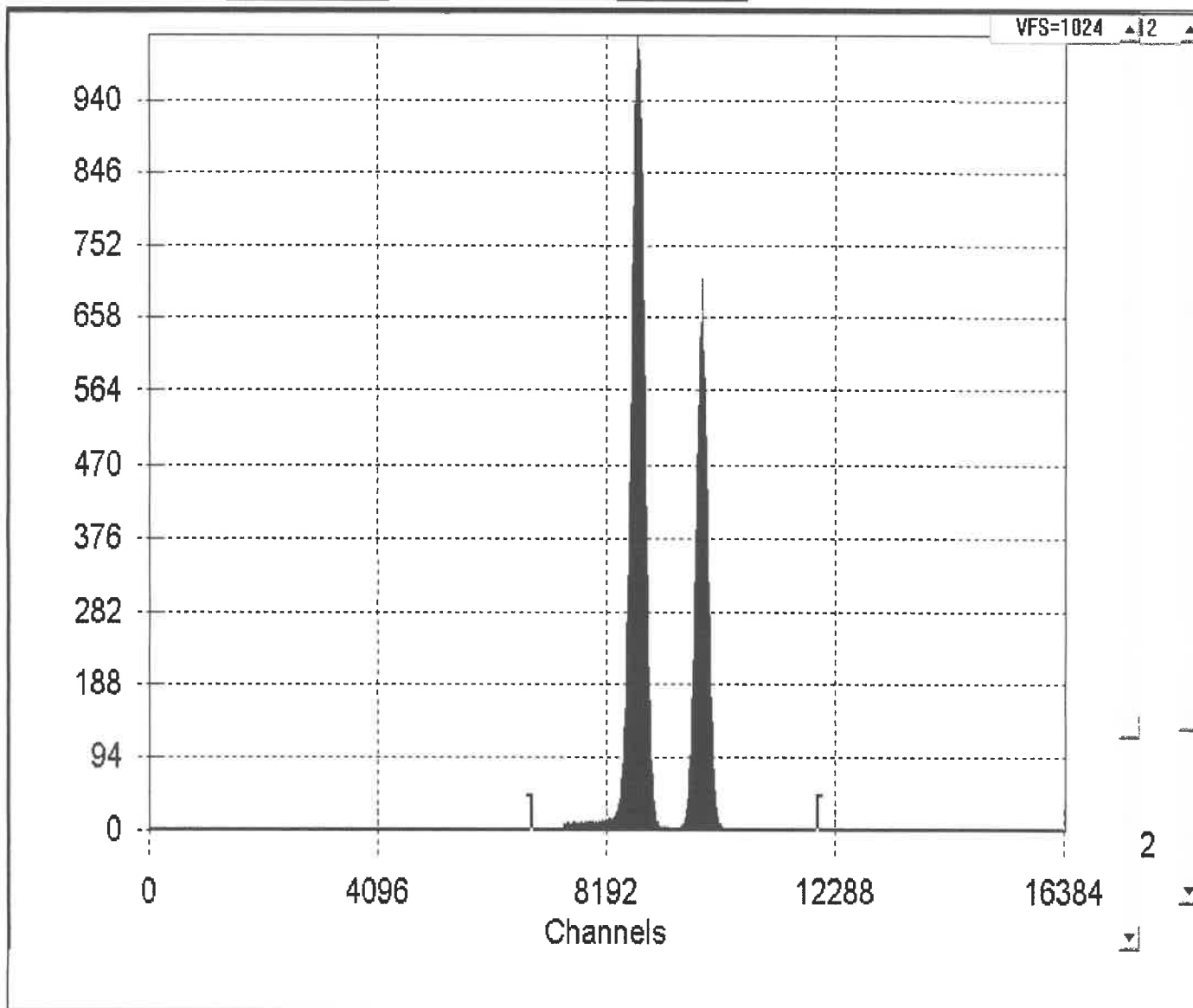
Am 241

Rise Time

1

Flat Top

0



BIAS VOLTS= **30** V

Leakage **28** nA